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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10659011
	Filing Date		2003-09-10
	First Named Inventor	Paul I. Szabo	
	Art Unit	2474	
	Examiner Name	V. Louis	
	Attorney Docket Number	1361008-2012.3/10.035C3	

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5884040		1999-03-16	Chung	
	2	6359879		2002-03-19	Carvey et al.	
	3	6601084		2003-07-29	Bhaskaran et al.	
	4	6651099		2003-11-18	Dietz et al.	
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9	6980521	2005-12-27	Jarvis
10	7020713	2006-03-28	Shah et al.
11	7051066	2006-05-23	Albert et al.
12	6064671	2000-05-16	Killian

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U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20050114186	A1	2005-05-26	Heinrich	

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FOREIGN PATENT DOCUMENTS

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1	F5 Networks, Inc., Big/ip Controller Administrator Guide, Version 3.1, 2000	<input type="checkbox"/>
2	F5 Networks, Inc., Big/ip Controller Reference Guide, Version 3.1, 2000	<input type="checkbox"/>
3	F5 Networks, Inc., Big/ip Controller Release Note, Version 3.1, April 28, 2000 http://support.f5.com/kb/en-us/archived_products/big-ip/releases/notes/product/relnotes3_1	<input type="checkbox"/>
4	Foundry Networks, Inc., Application Note: Firewall Load Balancing with ServerIron, created March 3, 2000 http://www.foundrynet.com/pdf/an-firewall-load-bal.pdf	<input type="checkbox"/>
5	Foundry Networks, Inc., Foundry Products, http://www.foundrynet.com/appnotes , archived on May 10, 2000 http://replay.waybackmachine.org/20000510161749/http://www.foundrynet.com/appnotes	<input type="checkbox"/>
6	Wolf et al., Design Tradeoffs for Embedded Network Processors, Washington University in St. Louis, July 10, 2000	<input type="checkbox"/>
7	F5 Networks, Inc., BIG/ip Controller Getting Started Guide, Version 3.1, 2000	<input type="checkbox"/>
8	Request for Inter Partes Reexamination of U.S. Patent No. 7,102,996, U.S. Patent Serial No. 95/001,511 . filed December 13, 2010	<input type="checkbox"/>
9	Request for Inter Partes Reexamination of U.S. Patent No. 7,697,427, U.S. Patent Serial No. 95/001,509, filed December 13, 2010	<input type="checkbox"/>
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12	Office Communication for U.S. Patent Serial No. 95/001,511, mailed January 31, 2011	<input type="checkbox"/>
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Examiner Signature	/Vinncelas Louis/ (02/15/2011)	Date Considered	02/15/2011
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